

Amendments to the Figures

The attached sheet of figures includes changes to Figs. 3, 4 and 7. In order to meet the drawing requirements set forth in PTO-948, Figures 3, 4, and 7 have been reformatted as Figures 3A-3C, 4A-4C and 7A-7C, respectively. The specification has been amended accordingly. No new matter has been added. A complete set of formal drawings is attached as Appendix A.

Attachment: Replacement Sheets

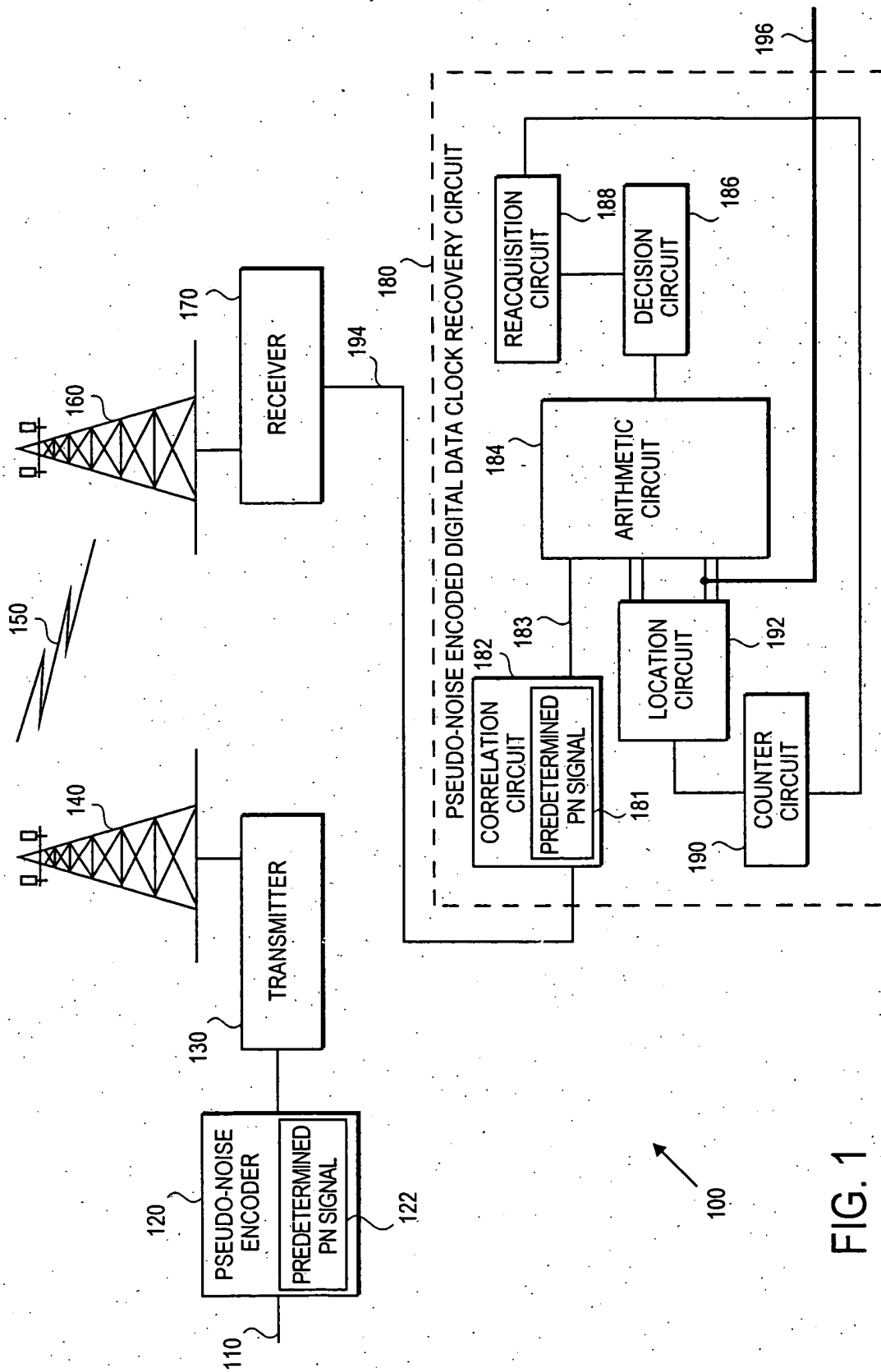


FIG. 1



200

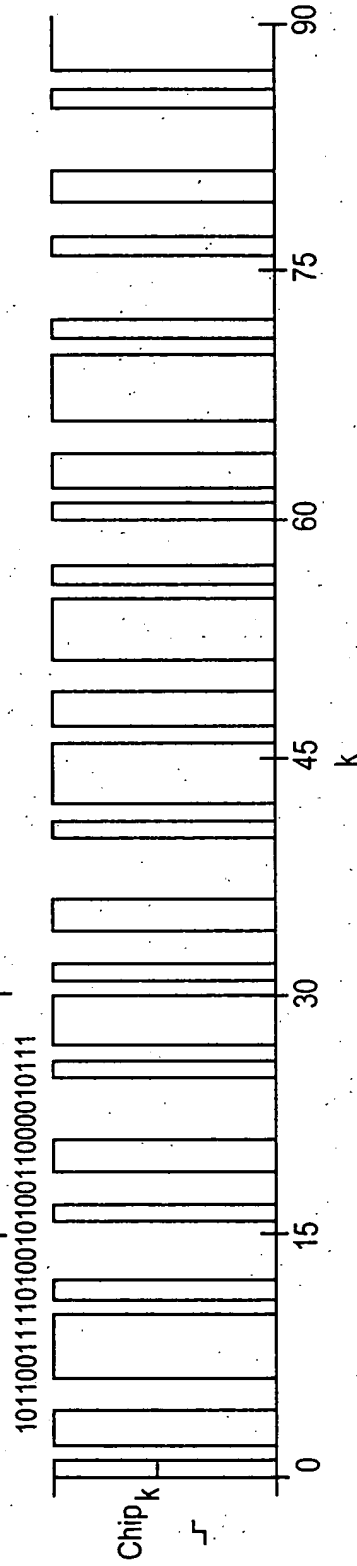
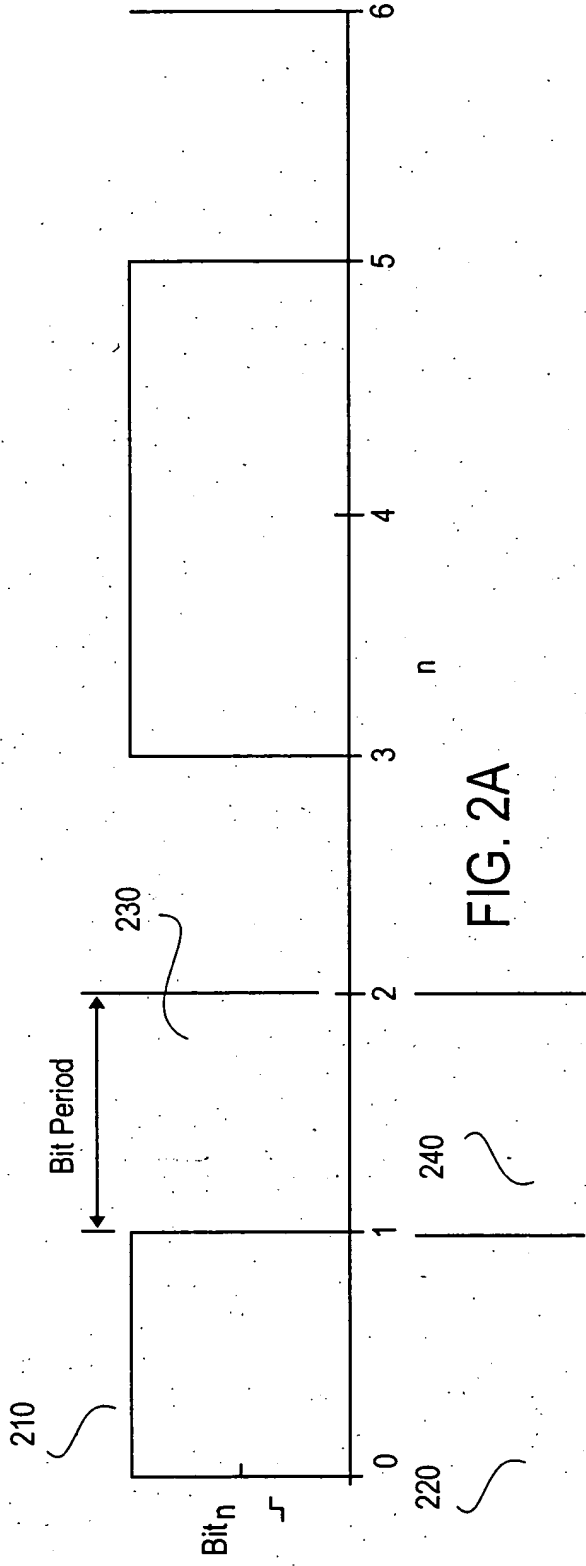


FIG. 2B

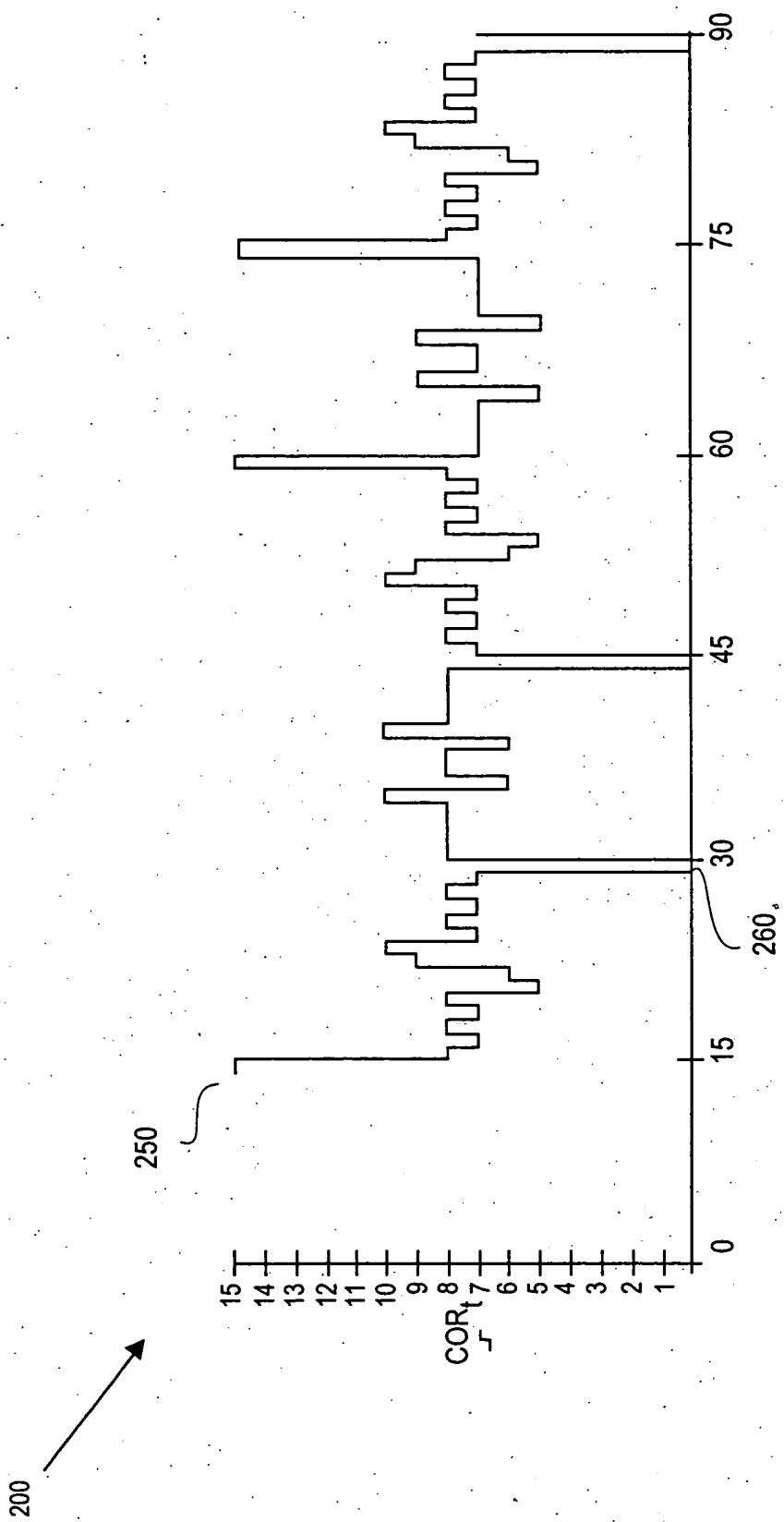
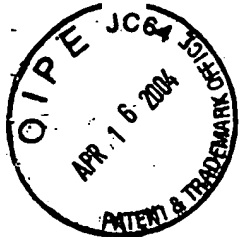
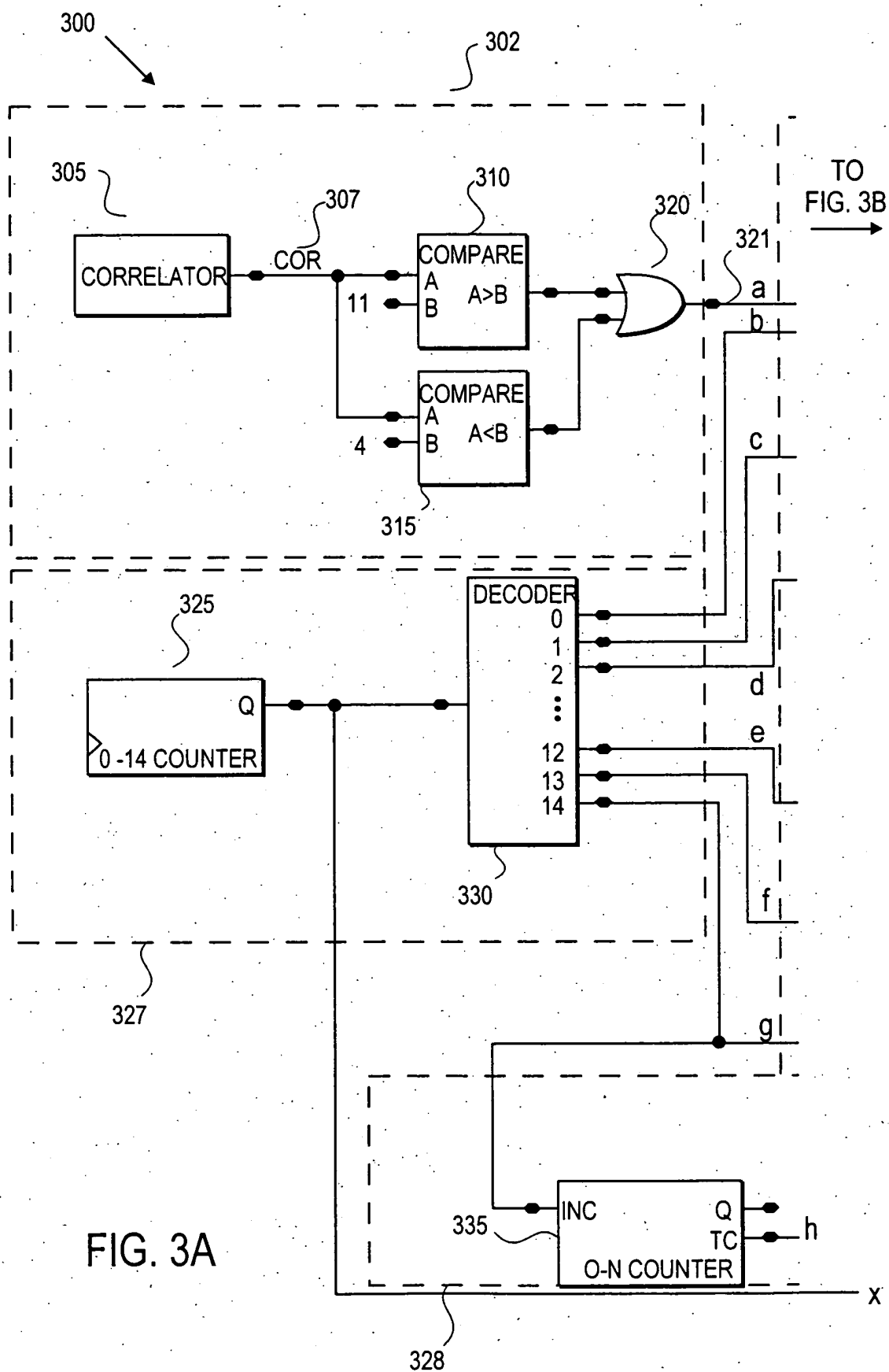


FIG. 2C



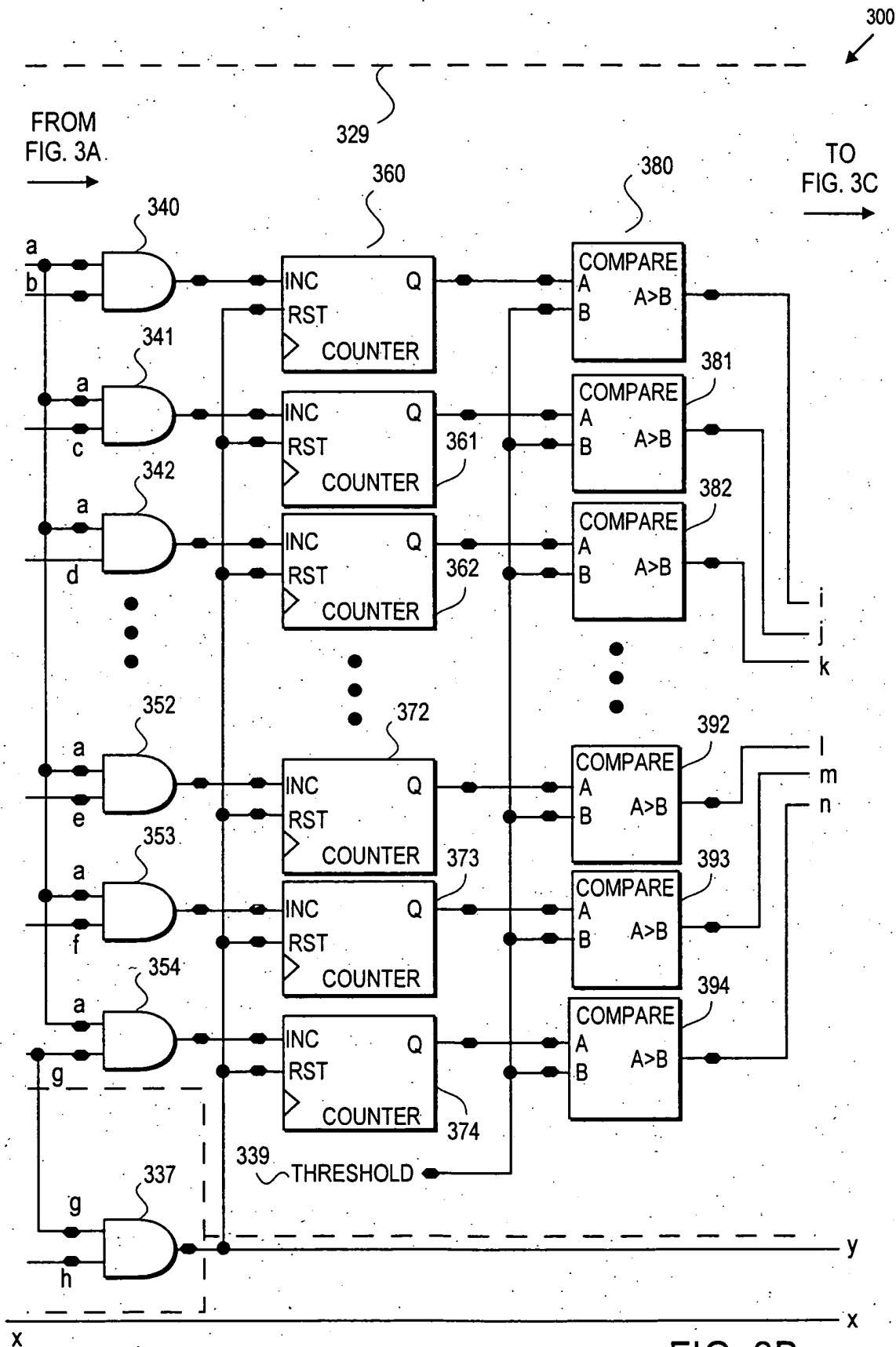


FIG. 3B

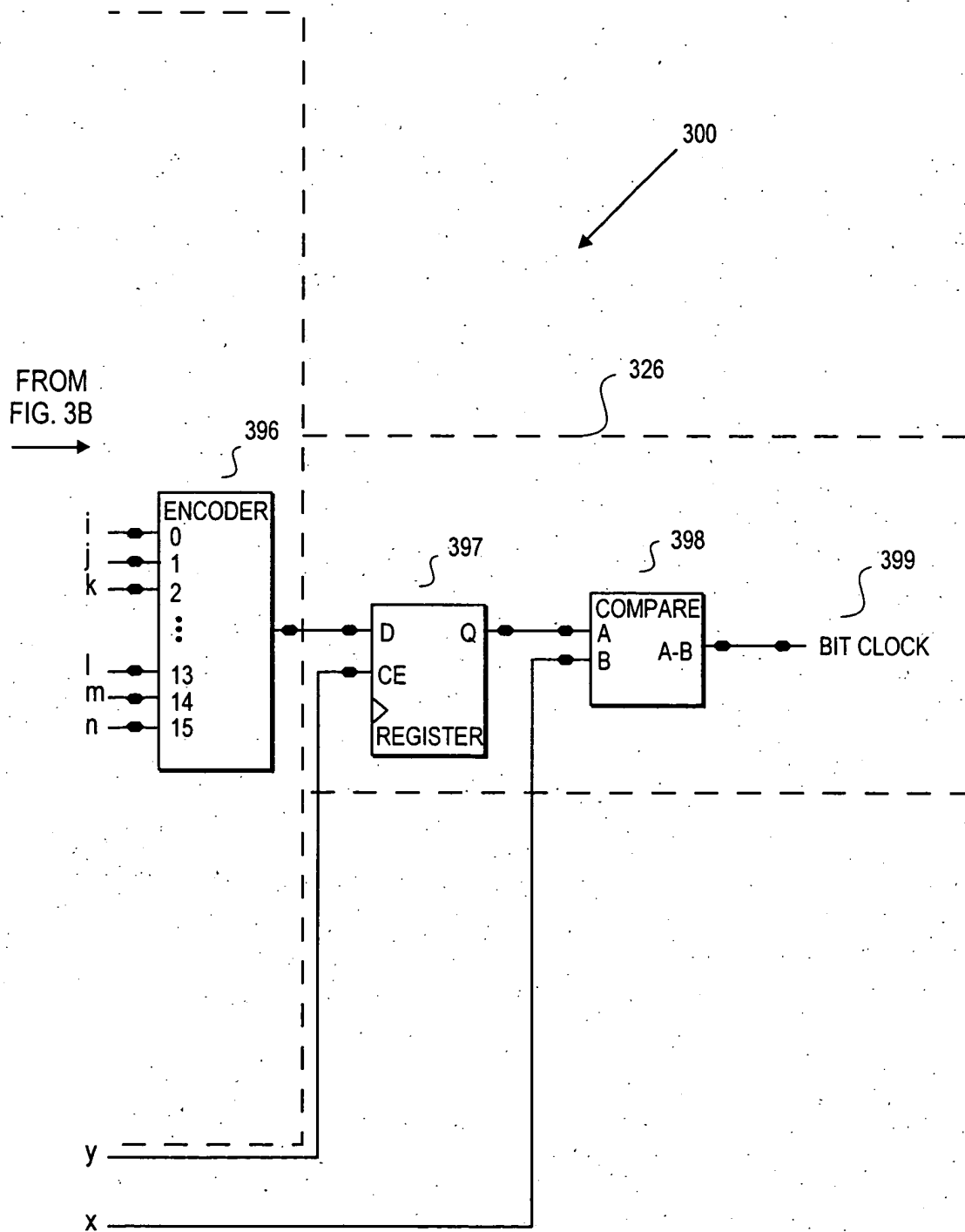


FIG. 3C

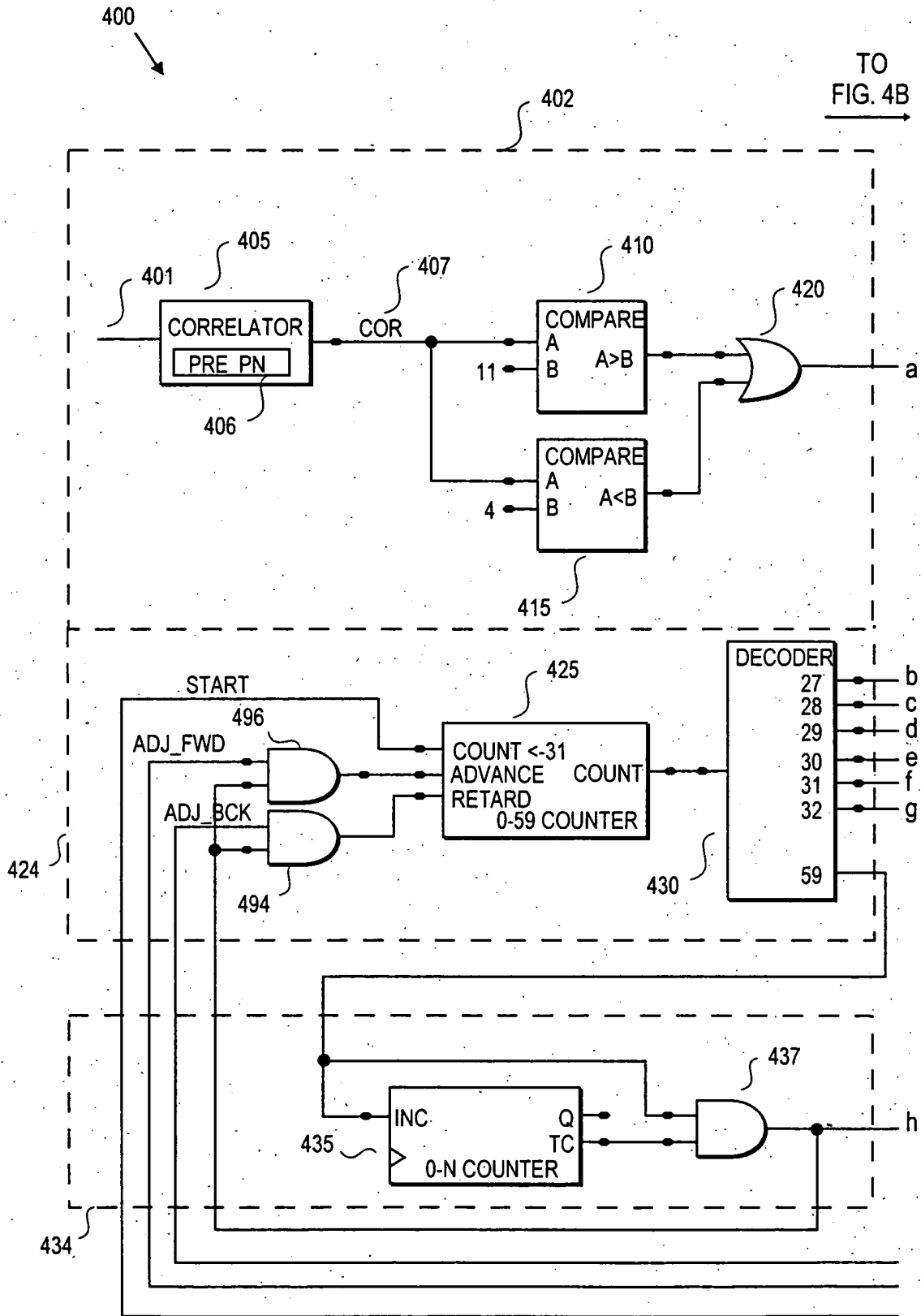
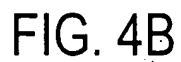


FIG. 4A



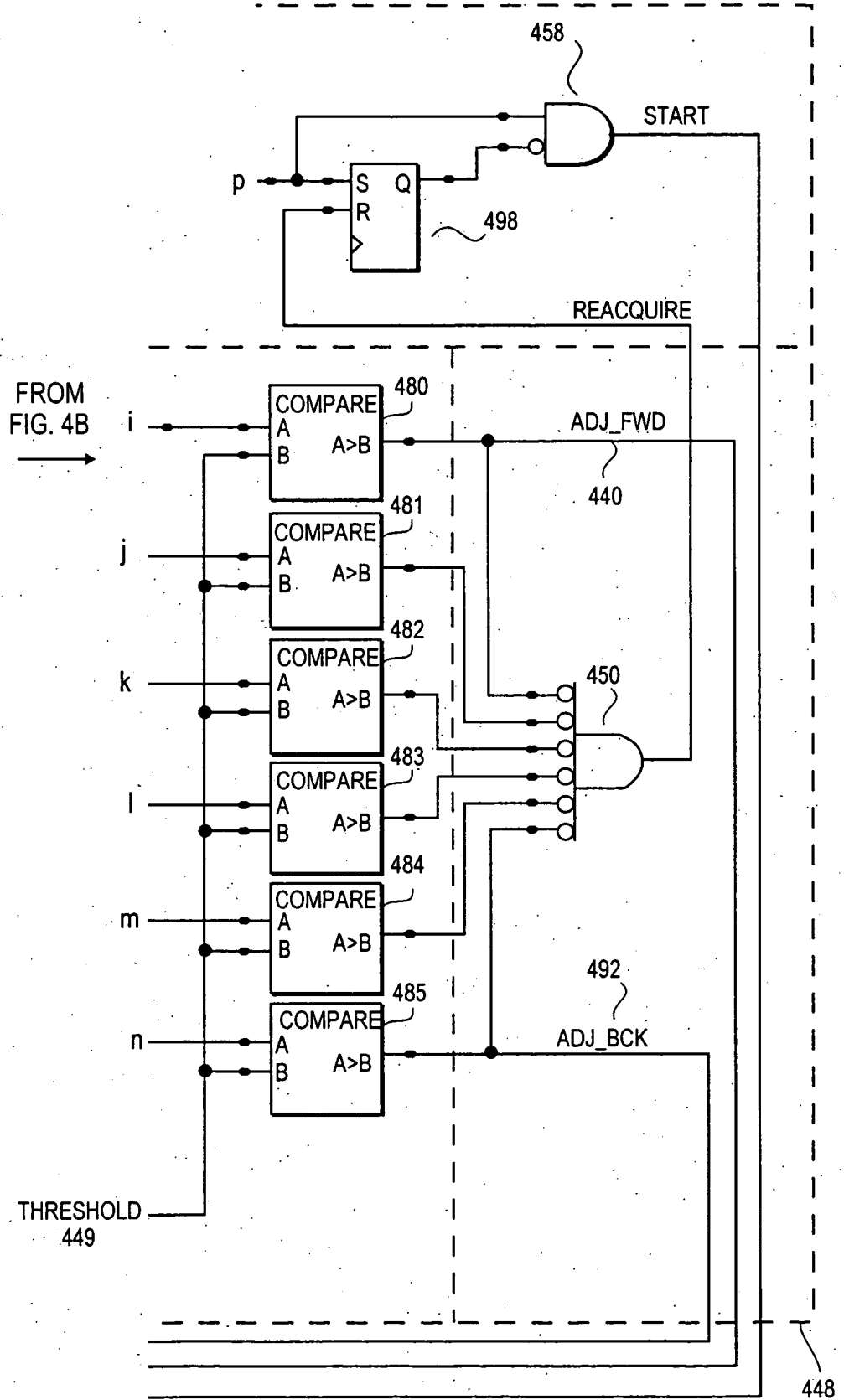
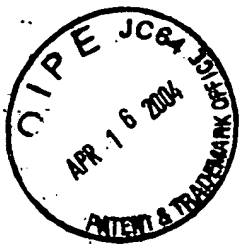


FIG. 4C

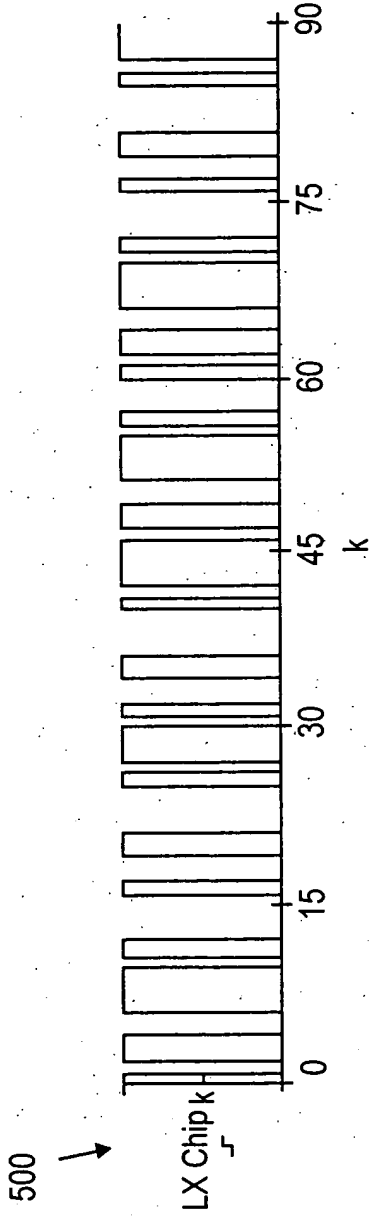


FIG. 5A

Chip Error Rate = 0.2

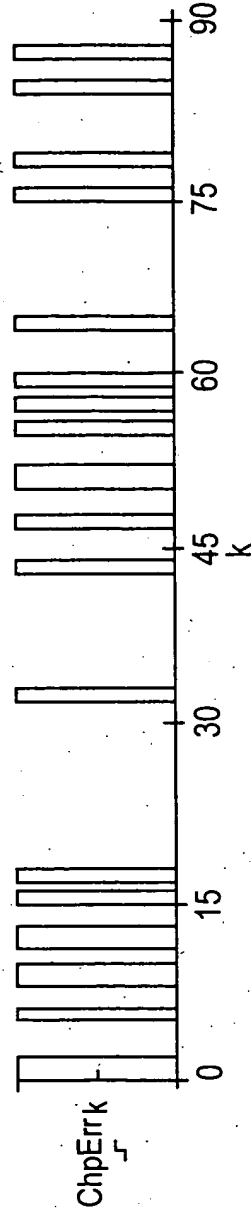


FIG. 5B

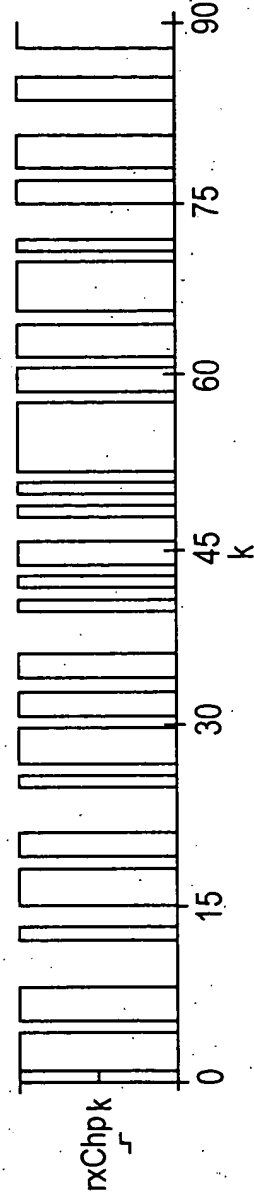


FIG. 5C

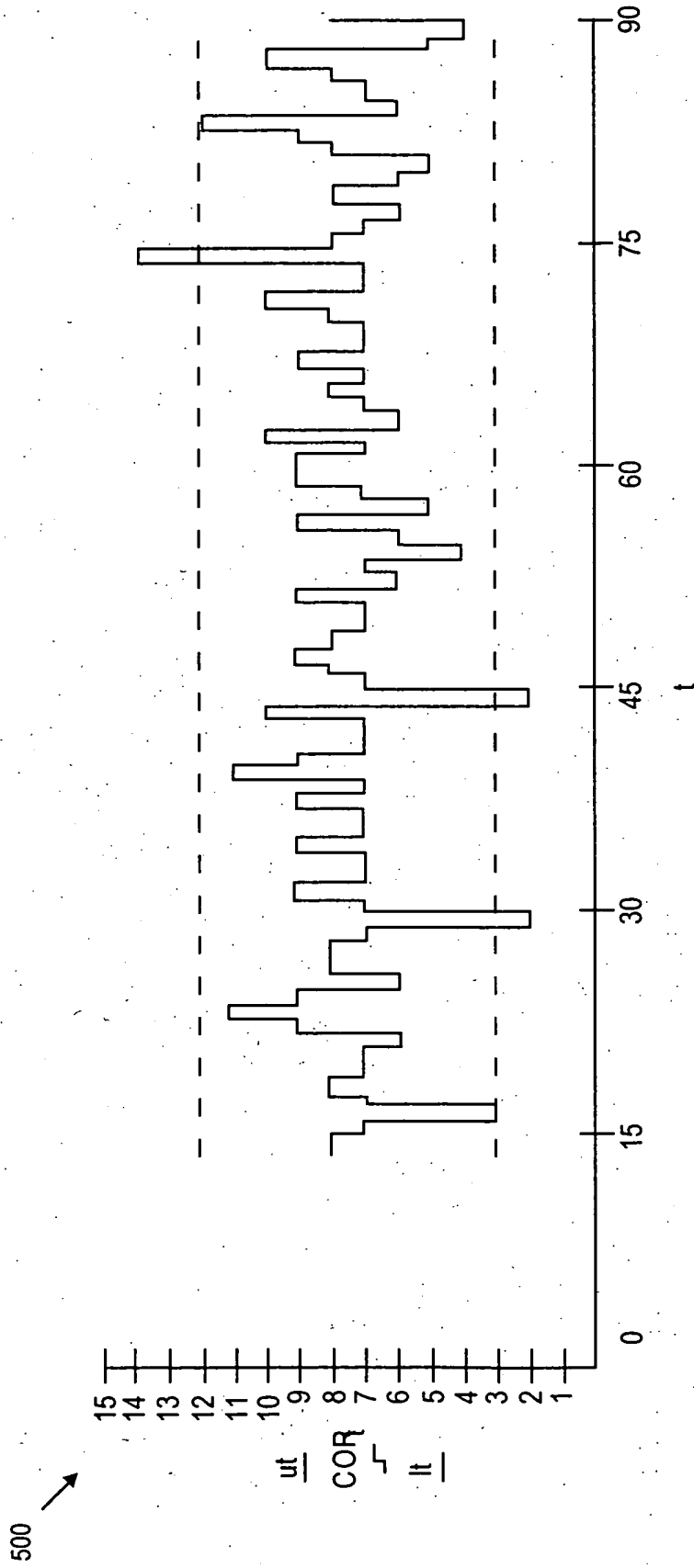
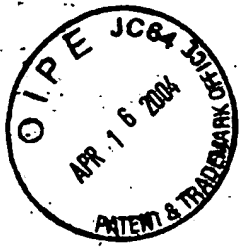
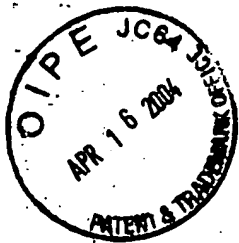


FIG. 5D



500

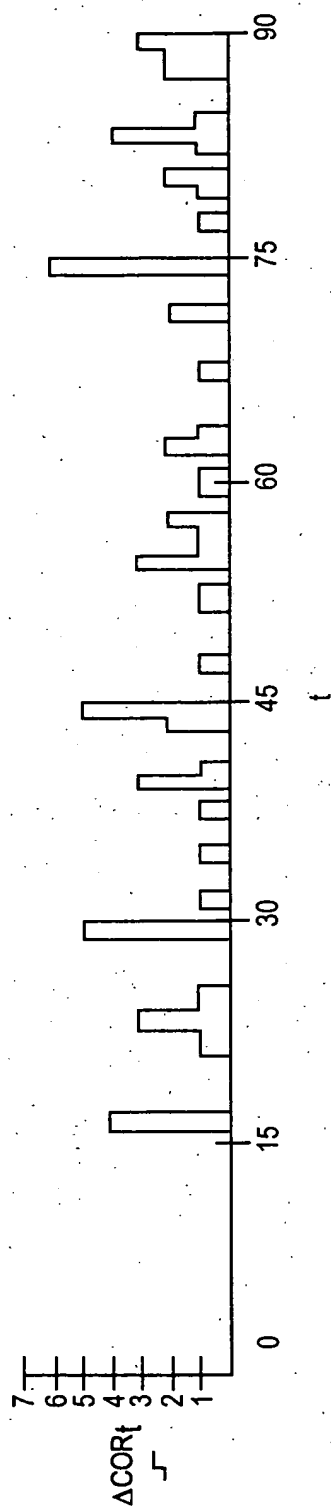


FIG. 5E

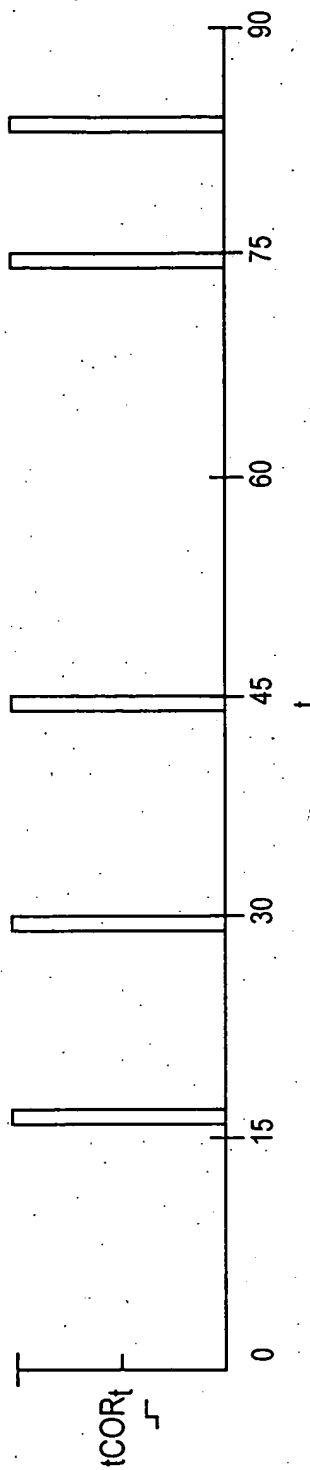


FIG. 5F

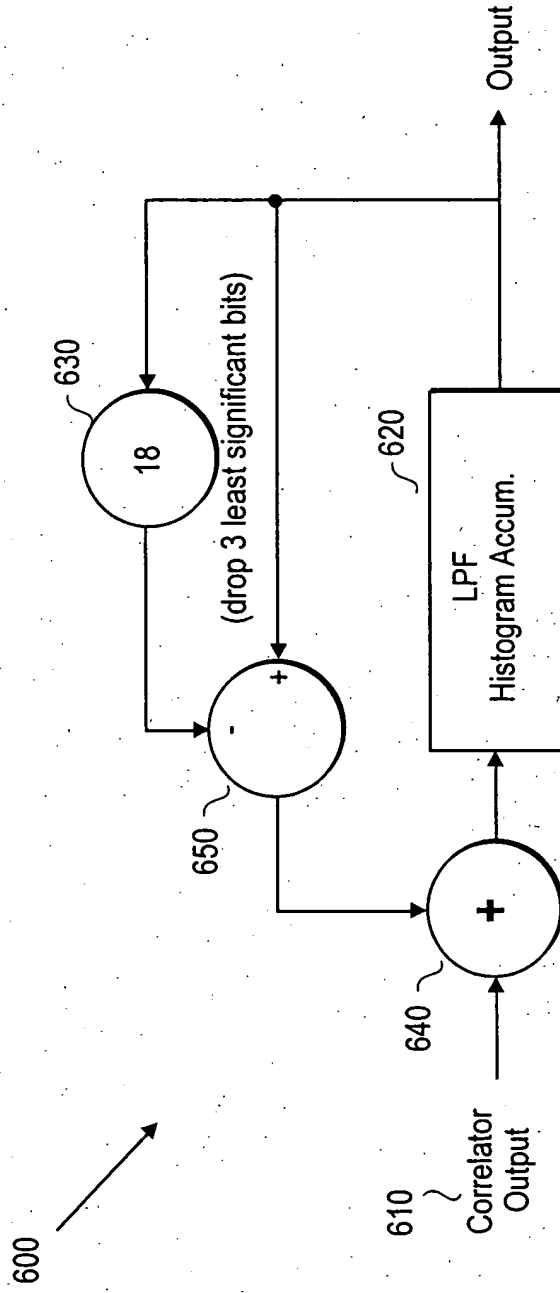
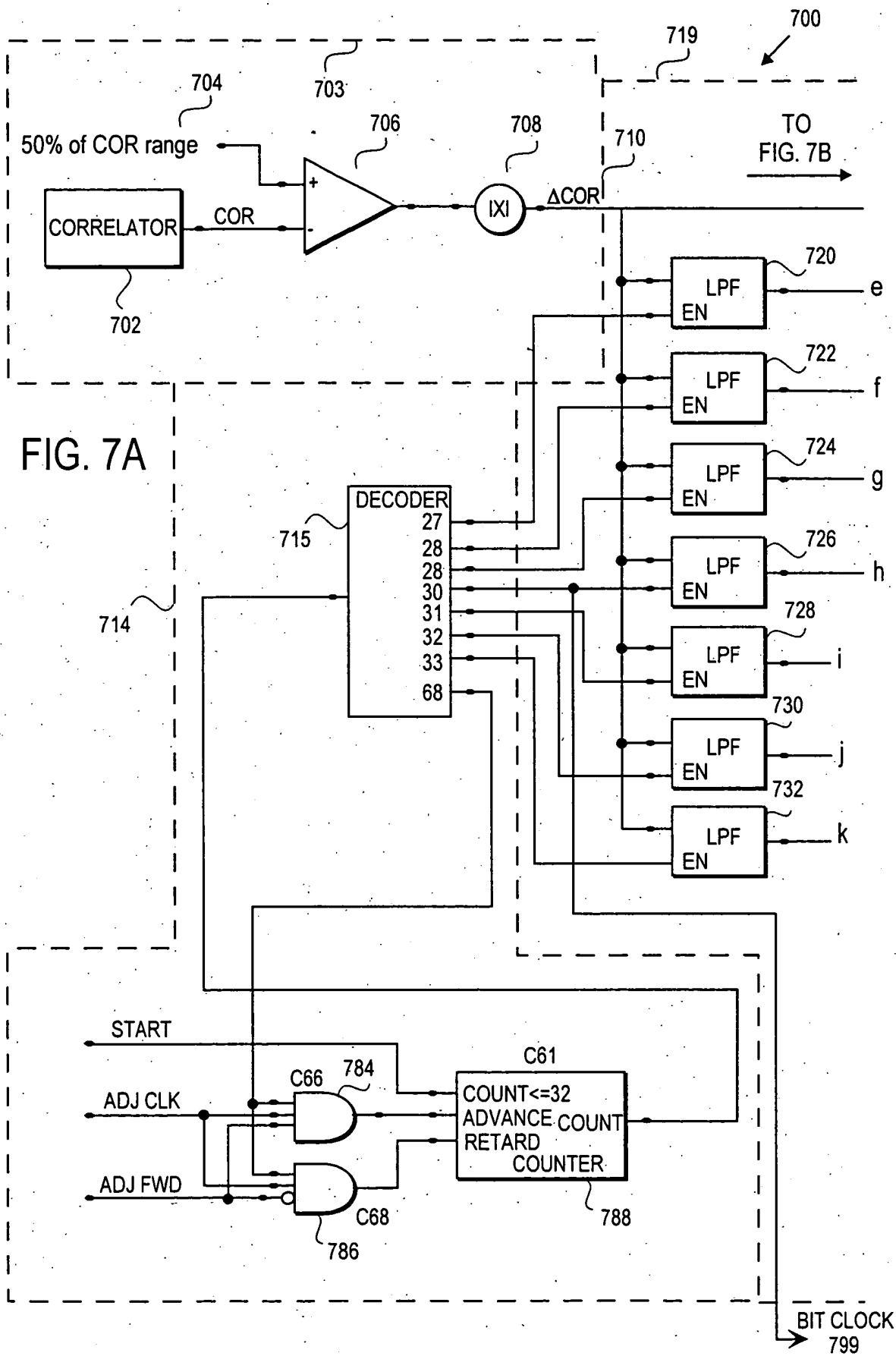


FIG. 6



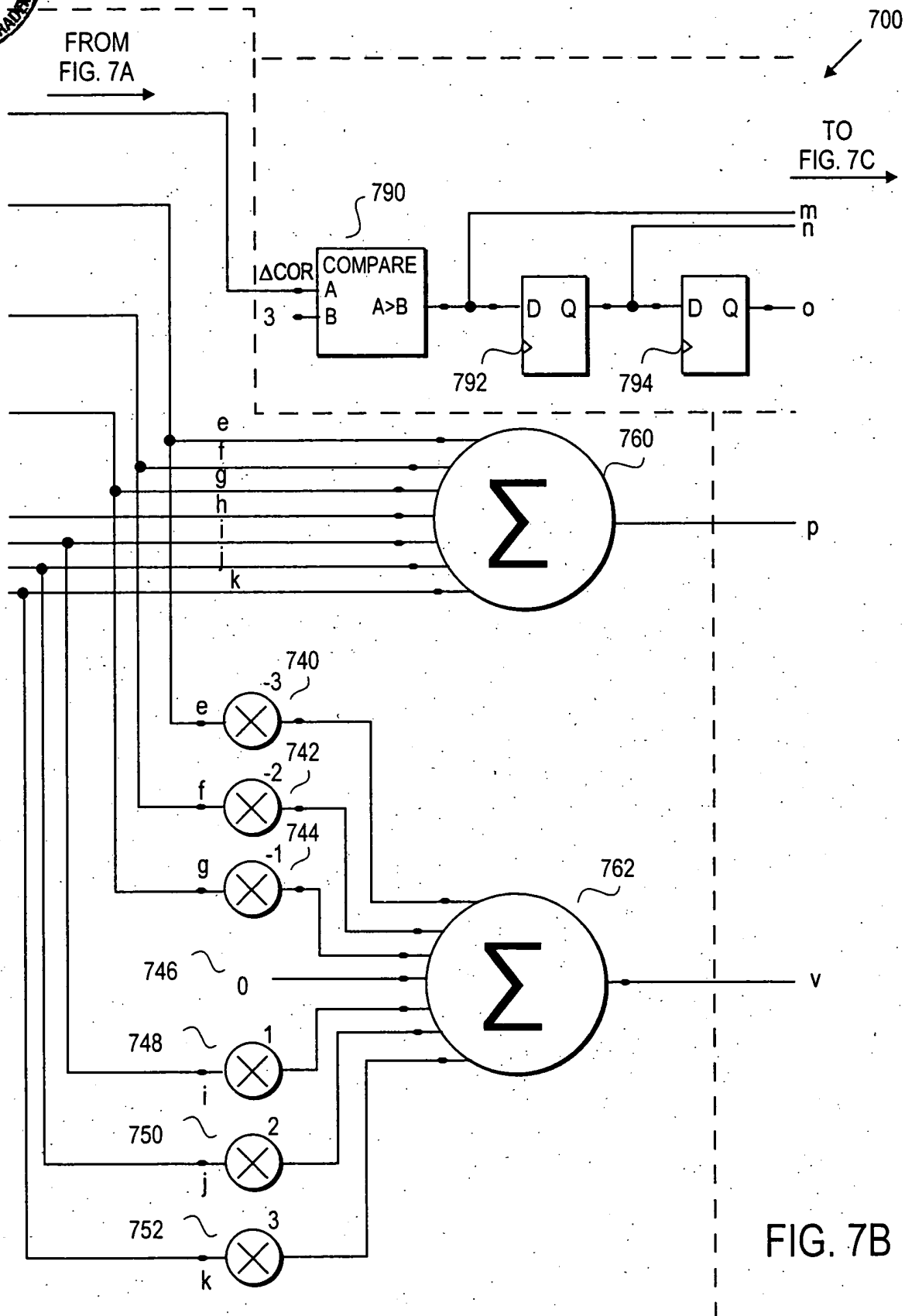


FIG. 7B

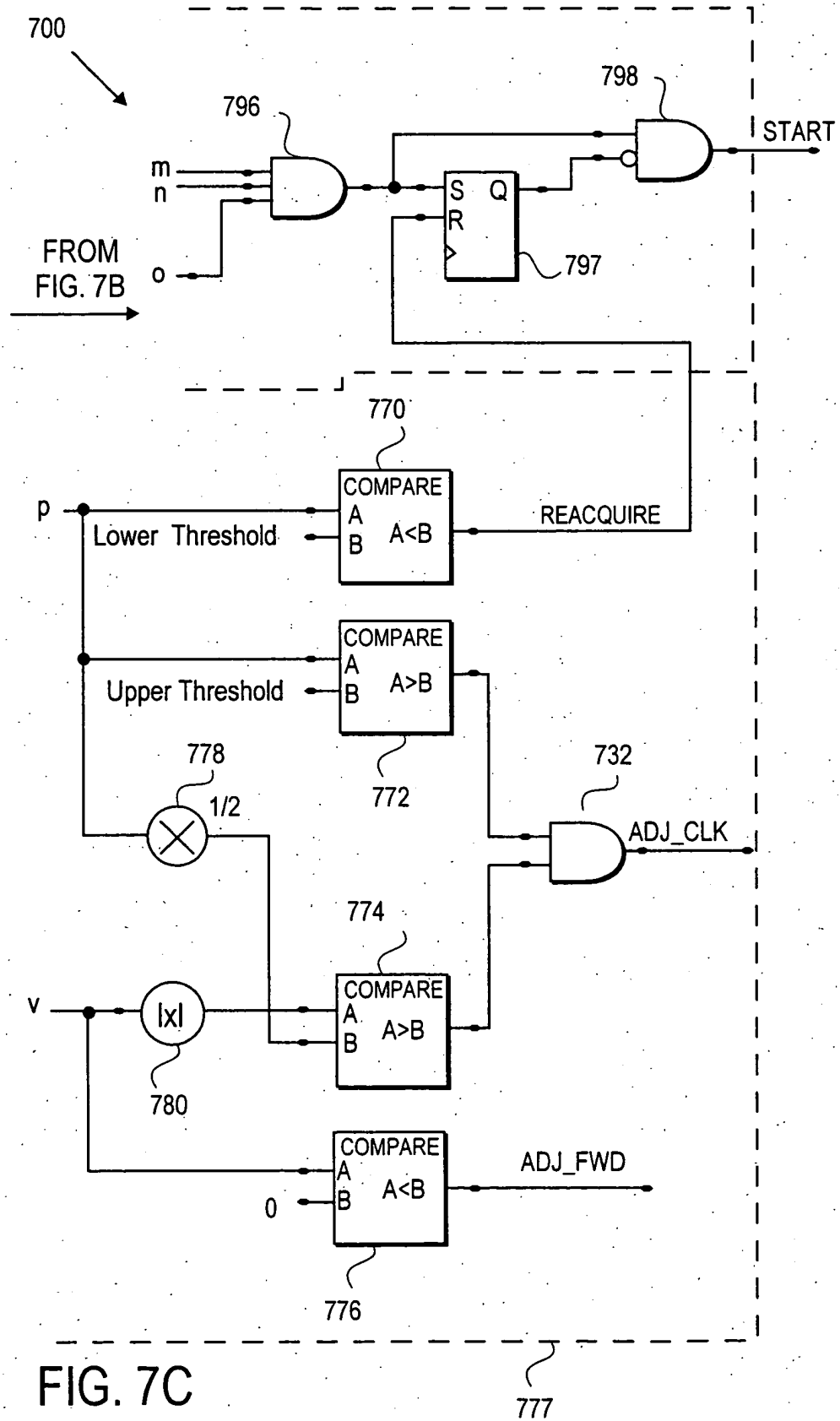


FIG. 7C

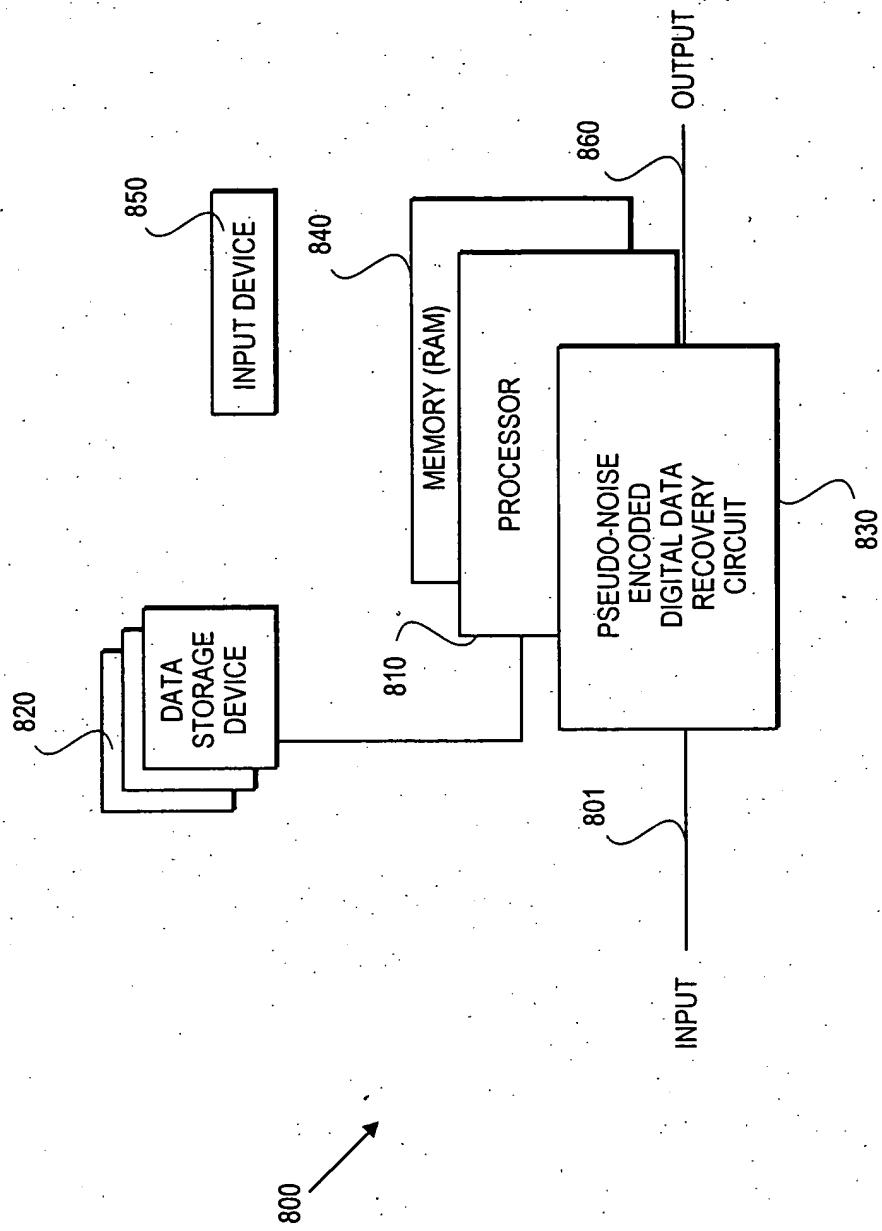
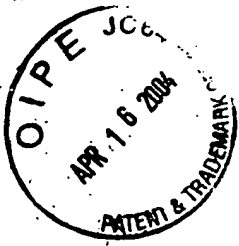
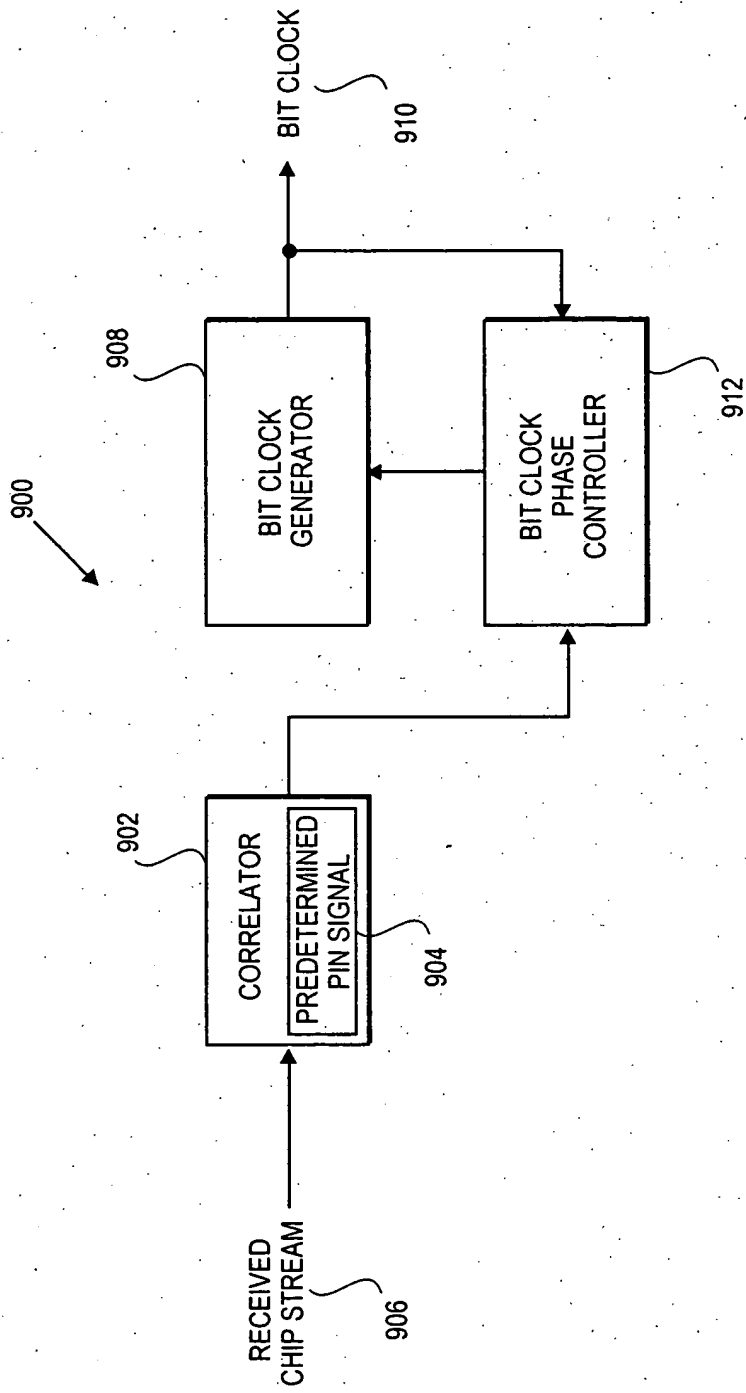
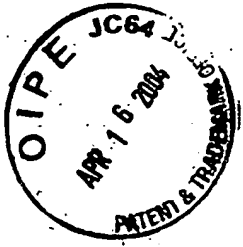
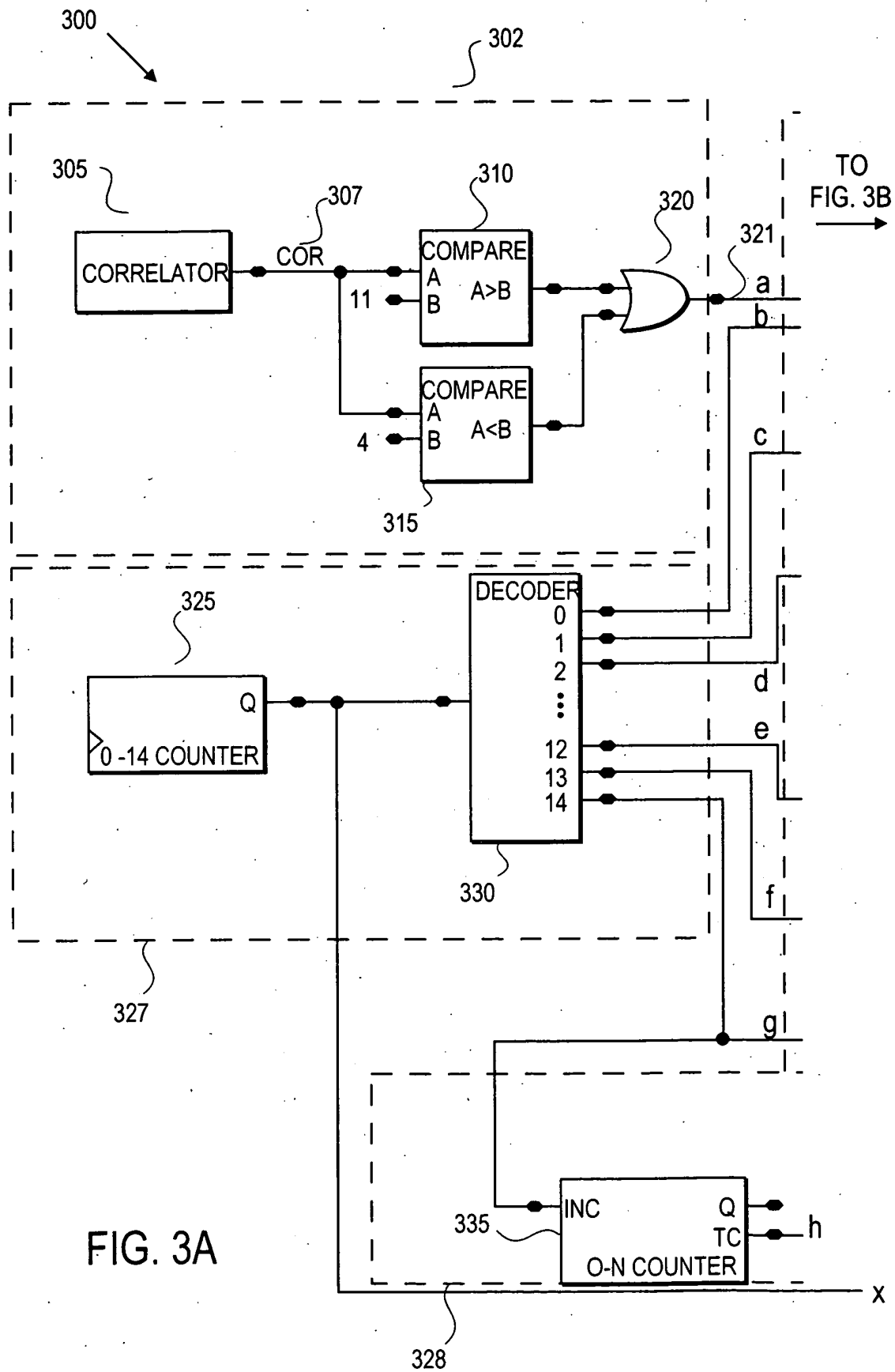


FIG. 8





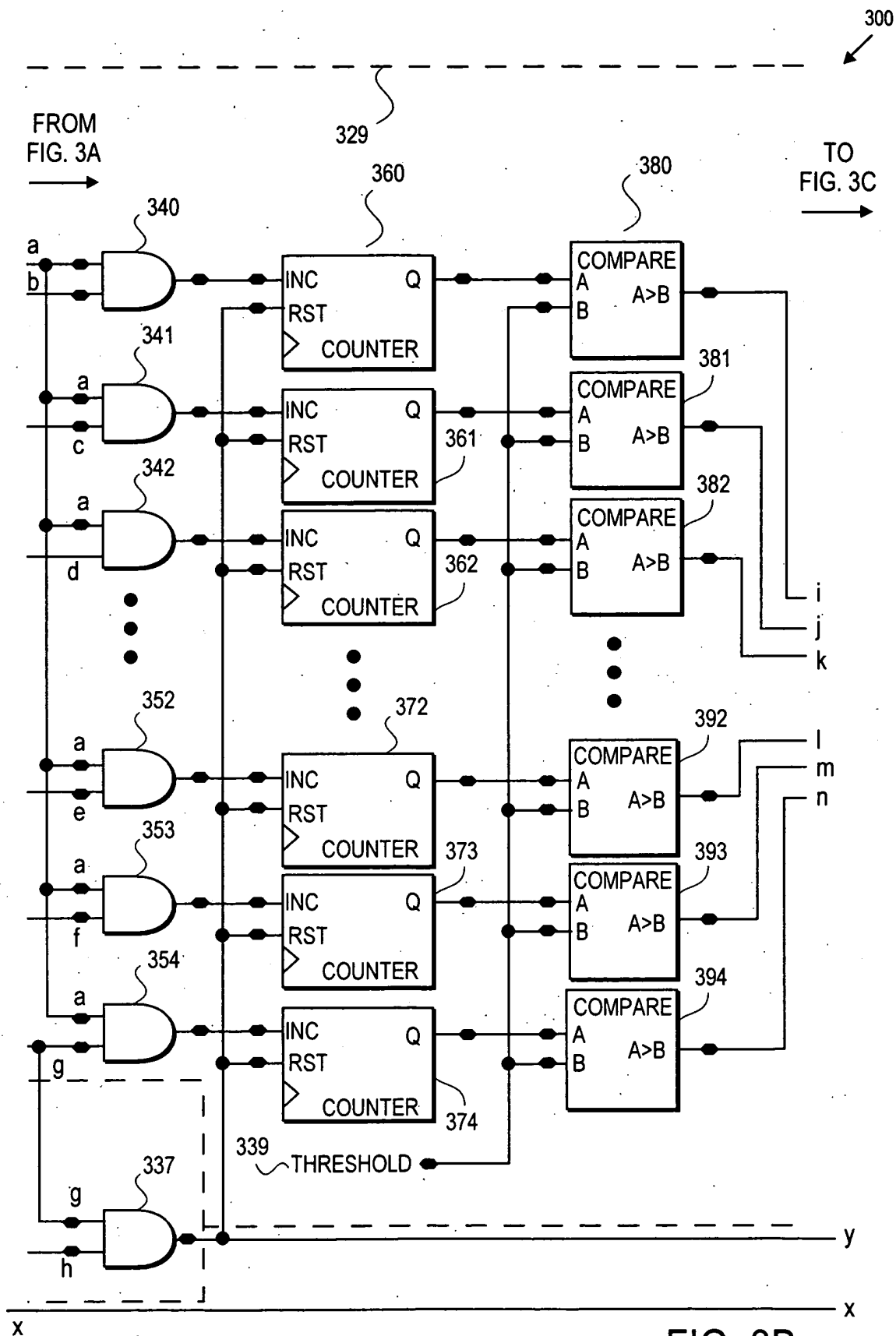


FIG. 3B

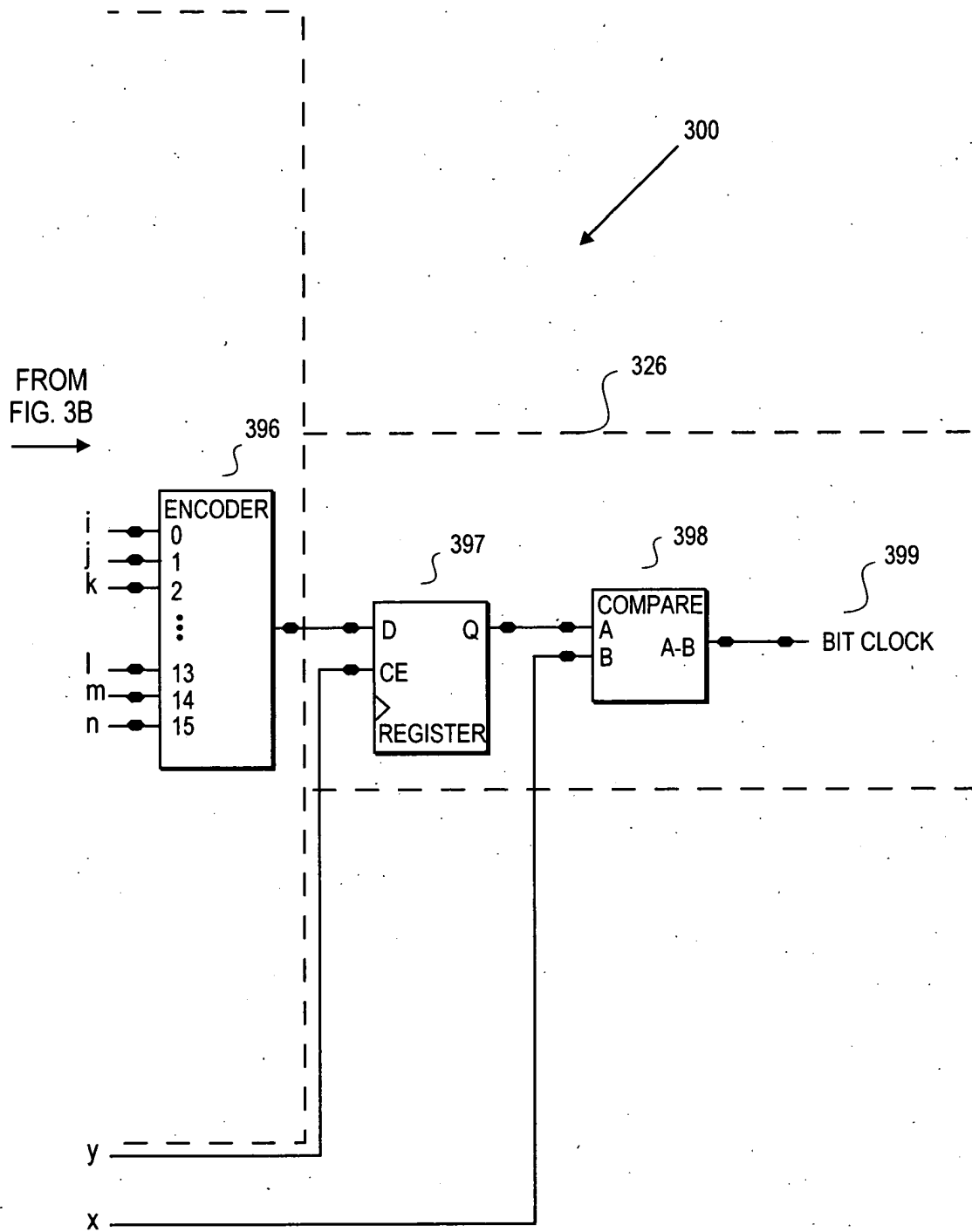


FIG. 3C

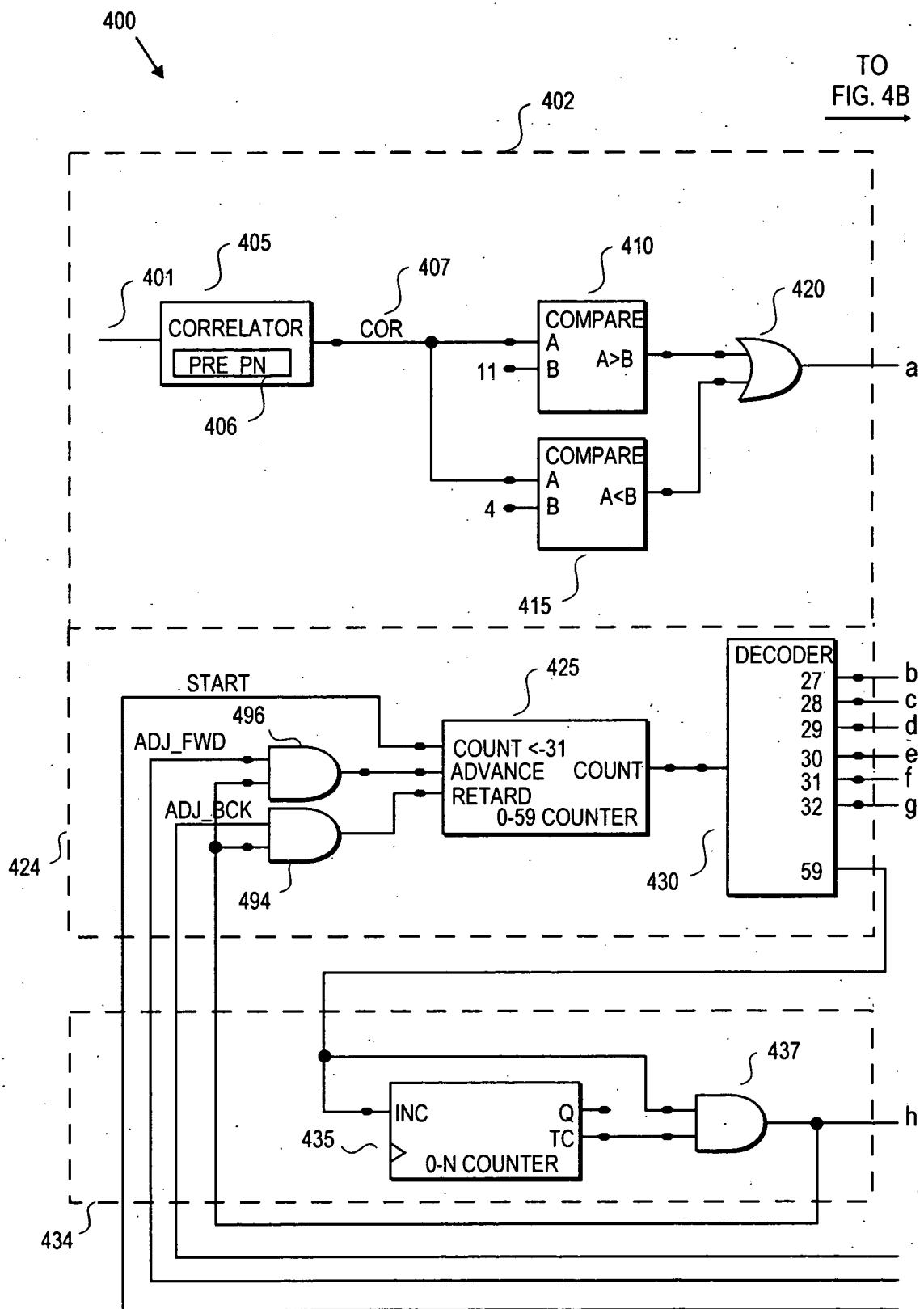
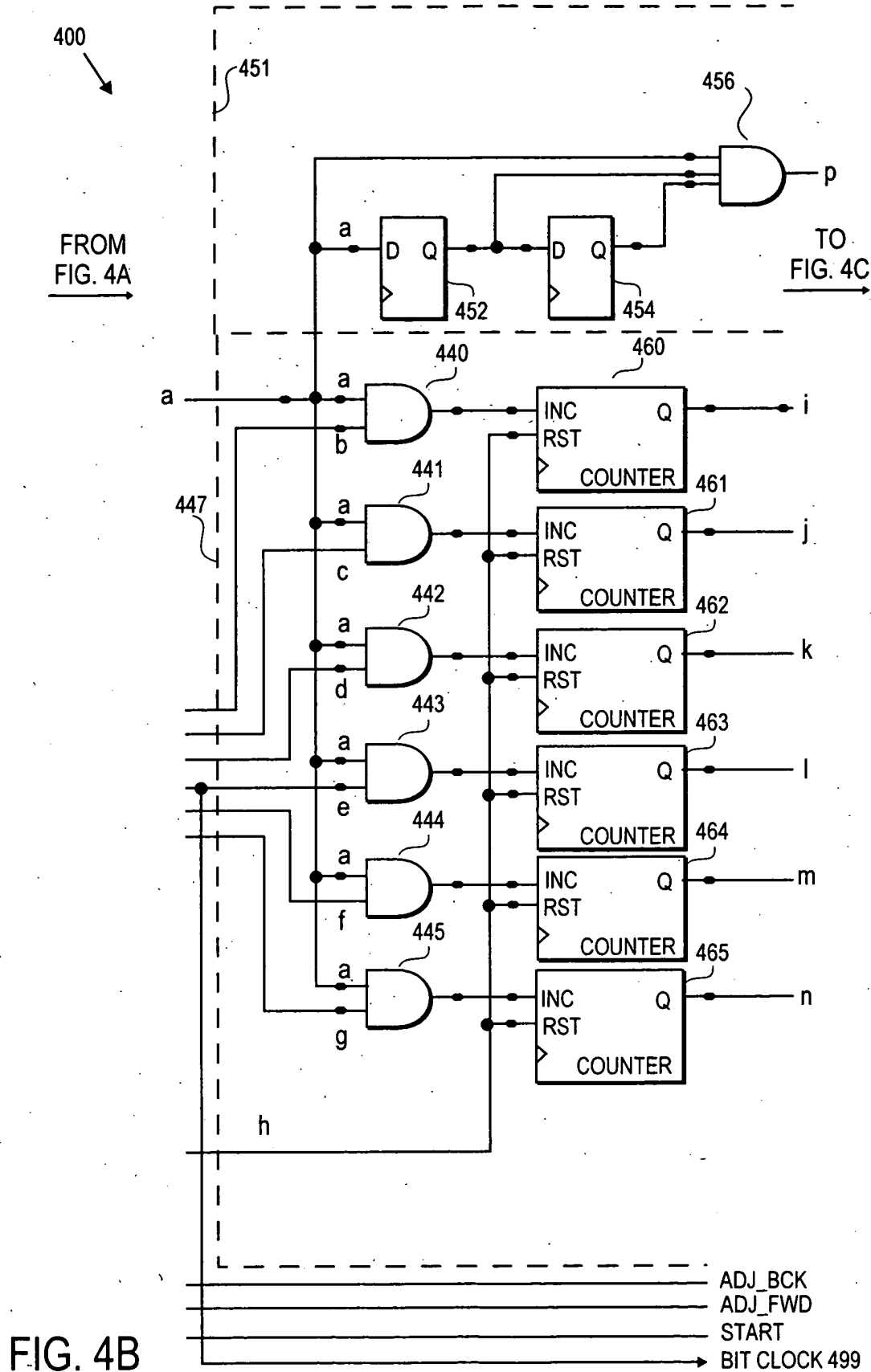


FIG. 4A



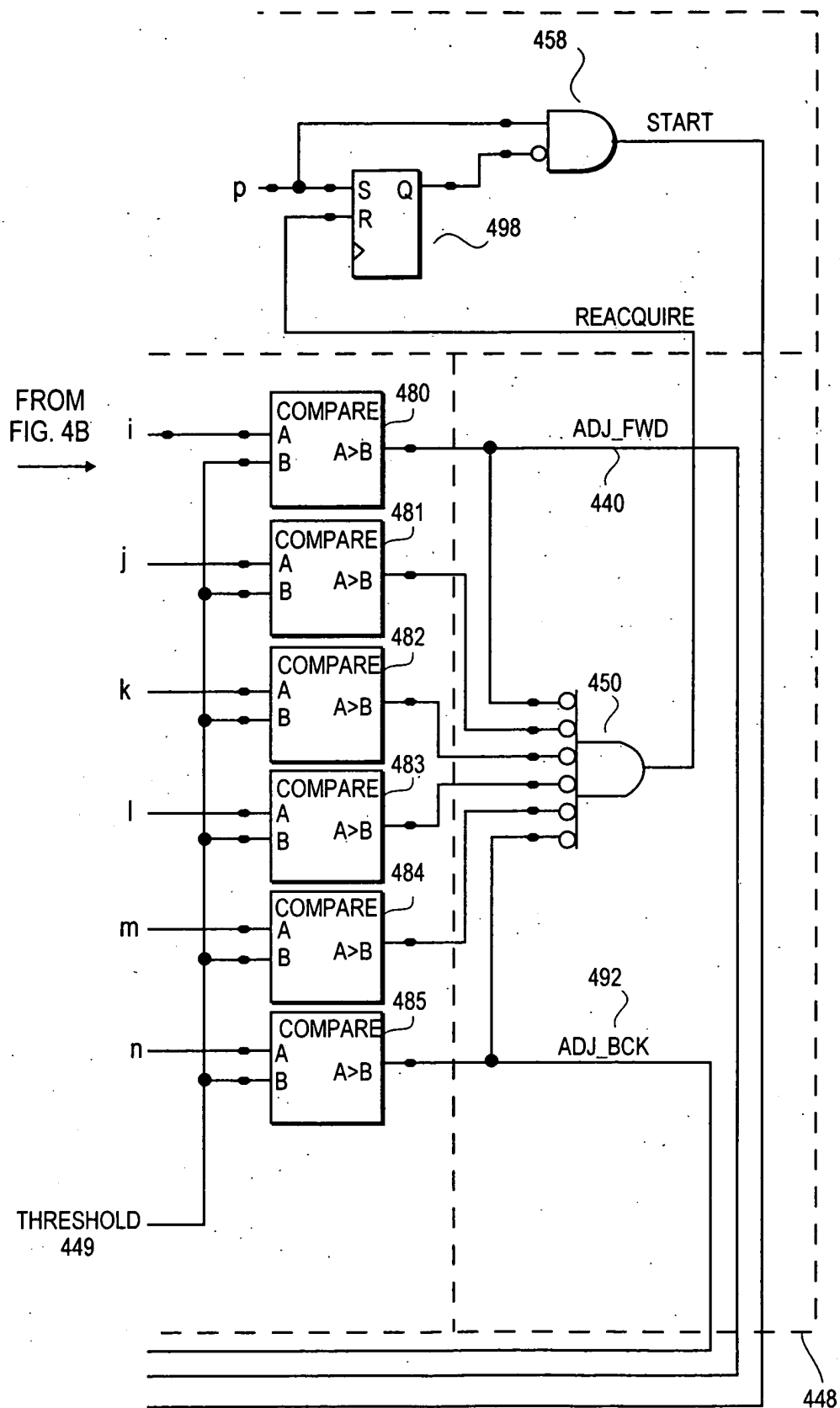
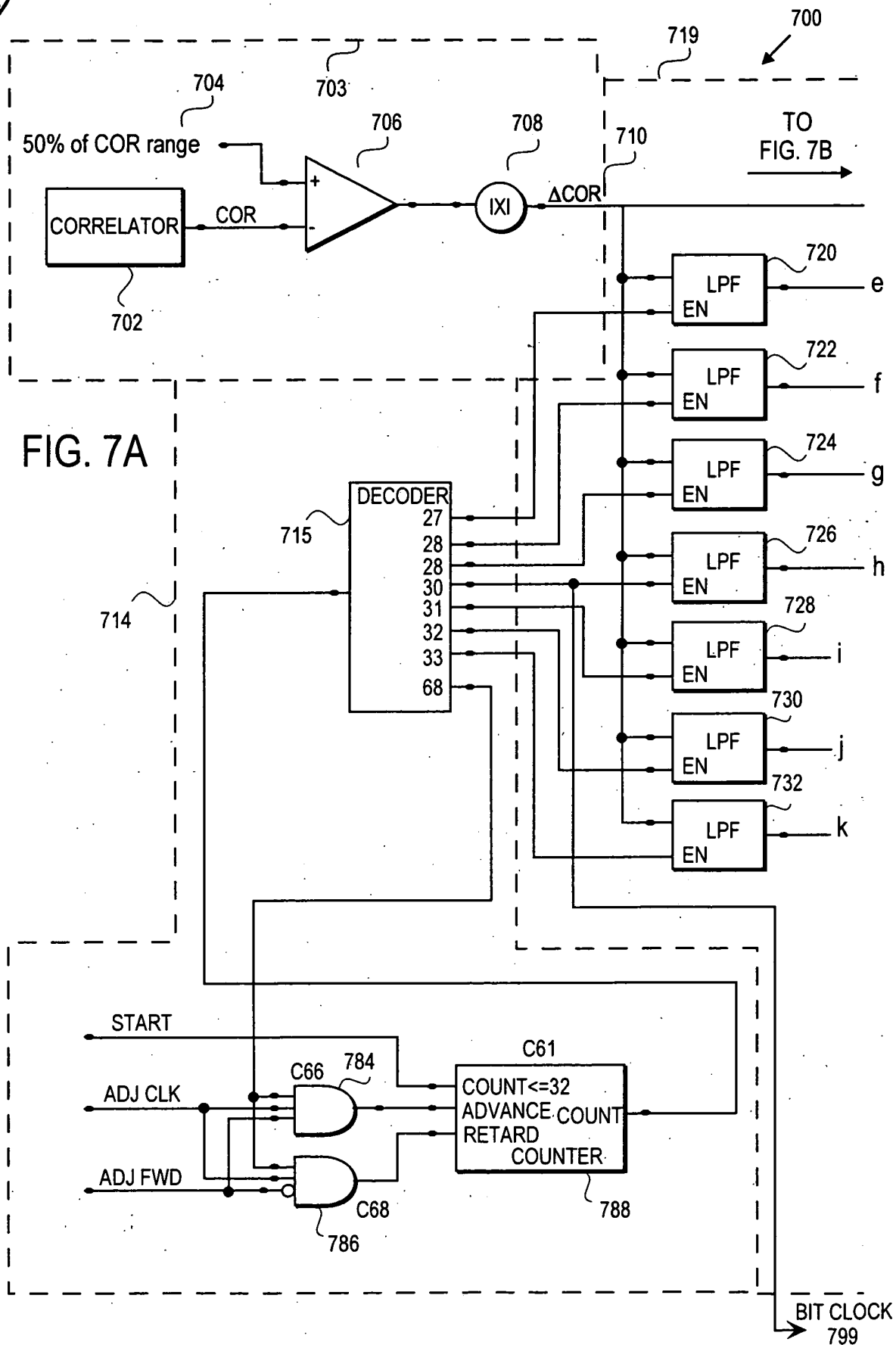


FIG. 4C



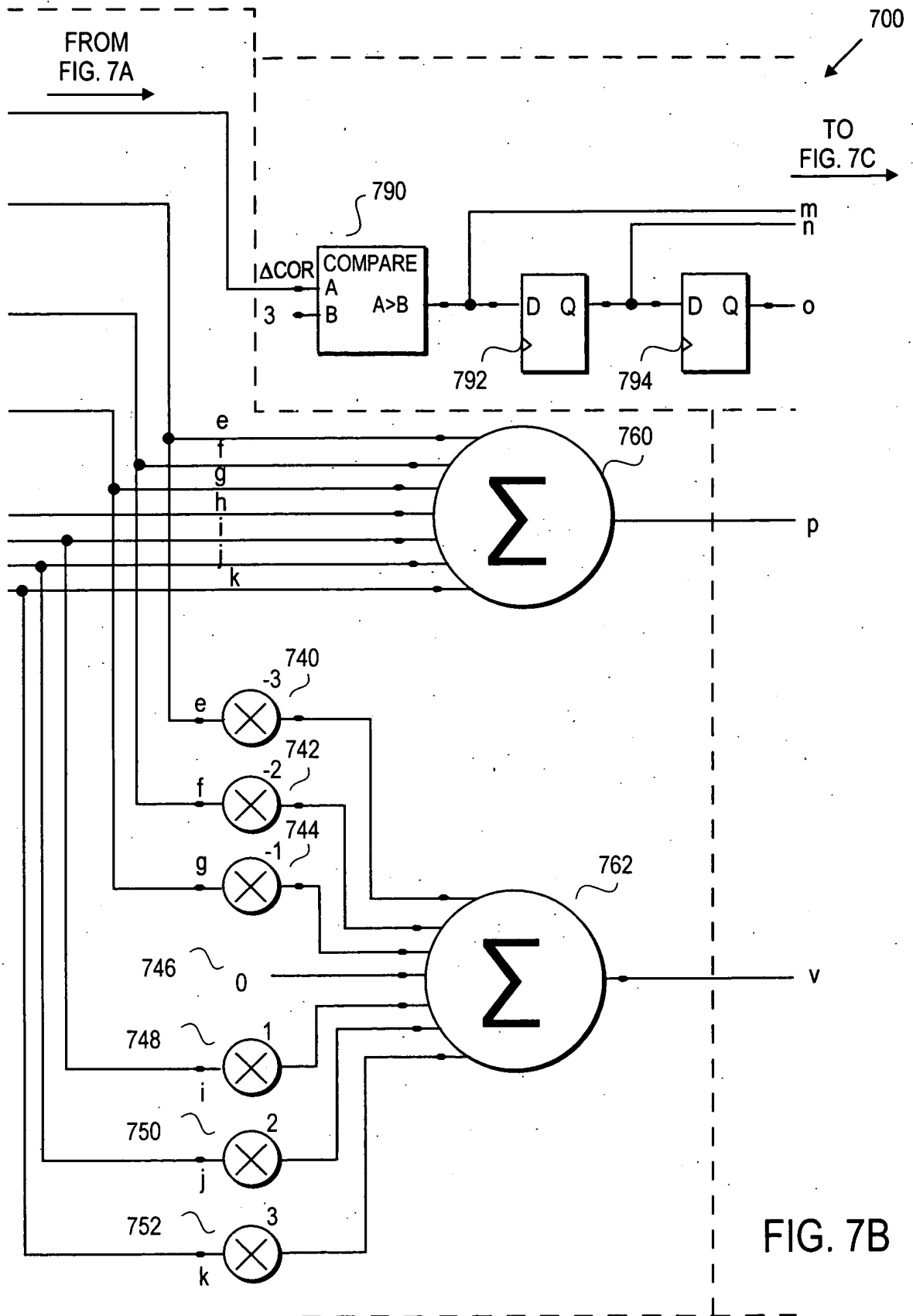


FIG. 7B

